



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

IN RE APPLICATION OF: Yoshiyuki MATSUNAGA, et al.

SERIAL NO: 09/927,632

GAU: 2612

FILED: August 13, 2001

EXAMINER: Aung Soe Moe

FOR: IMAGE SYSTEM, SOLID-STAGE IMAGING SEMICONDUCTOR INTEGRATED CIRCUIT DEVICE
USED IN THE IMAGE SYSTEM, AND DIFFERENCE OUTPUT METHOD USED FOR THE IMAGE
SYSTEM

**REQUEST FOR EXTENSION OF TIME
UNDER 37 C.F.R. 1.136**

COMMISSIONER FOR PATENTS
ALEXANDRIA, VIRGINIA 22313

SIR:

It is hereby requested that a **two** month extension of time be granted to July 9, 2005 for

- ☒ filing a response to the Official Action dated: February 9, 2005
- ☐ responding to the requirements in the Notice of Allowability dated:
- ☐ filing the Formal Drawings. The Issue Fee due has been timely filed.
- ☐ responding to the Notice to File Missing Parts of Application dated:
- ☐ filing a Notice of Appeal. A timely response to the final rejection, due has been filed.
- ☐ filing an Appeal Brief. A Notice of Appeal was filed on:
- ☐ Applicant claims small entity status. See 37 CFR 1.27. Therefore, the fee amount shown below is reduced by one-half.

The required fee of \$450.00 is enclosed herewith by credit card payment and any further charges may be made against the Attorney of Record's Deposit Account No. 15-0030. A duplicate copy of this sheet is enclosed.

07/13/2005 HALI11 00000124 09927632

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Respectfully Submitted,

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